

FIG. 1A

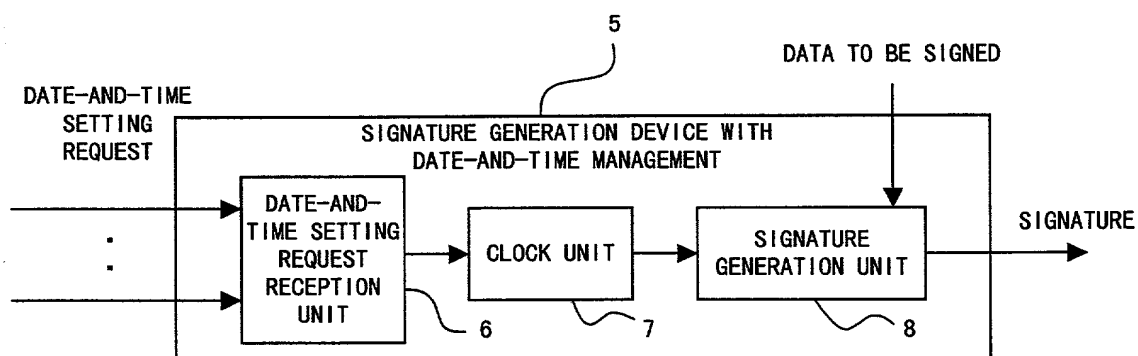
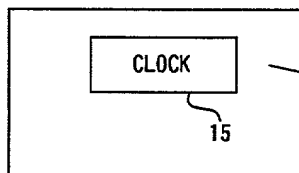
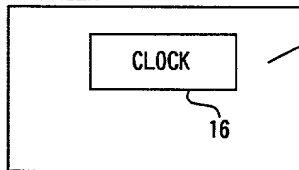


FIG. 1B

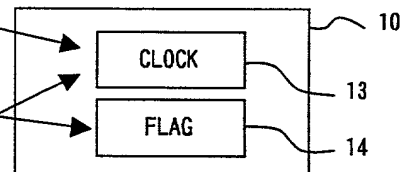
DATE-AND-TIME  
MANAGEMENT DEVICE OF  
DATE-AND-TIME MANAGER



DATE-AND-TIME  
MANAGEMENT DEVICE OF  
SPECIFIED DATE-AND-TIME  
MANAGER



DATE-AND-TIME  
MANAGEMENT DEVICE ON  
USER SIDE



(1) SETTING POSSIBLE  
(4) SETTING IMPOSSIBLE

(2) SETTING POSSIBLE  
(3) SETTING IMPOSSIBLE

FIG. 2

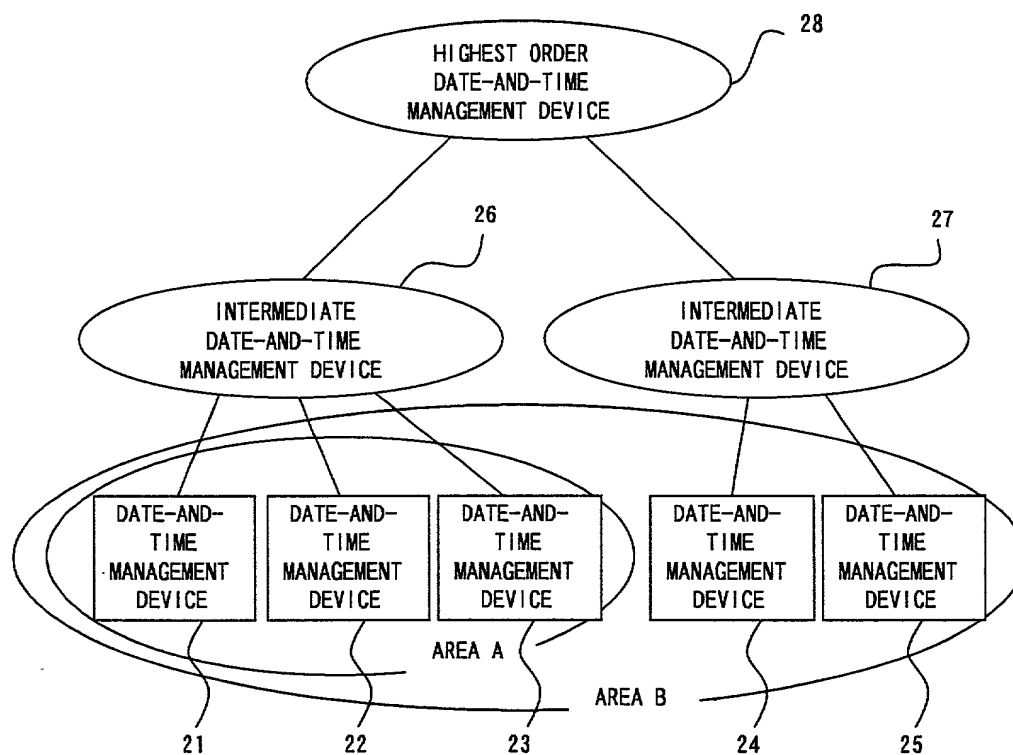


FIG. 3

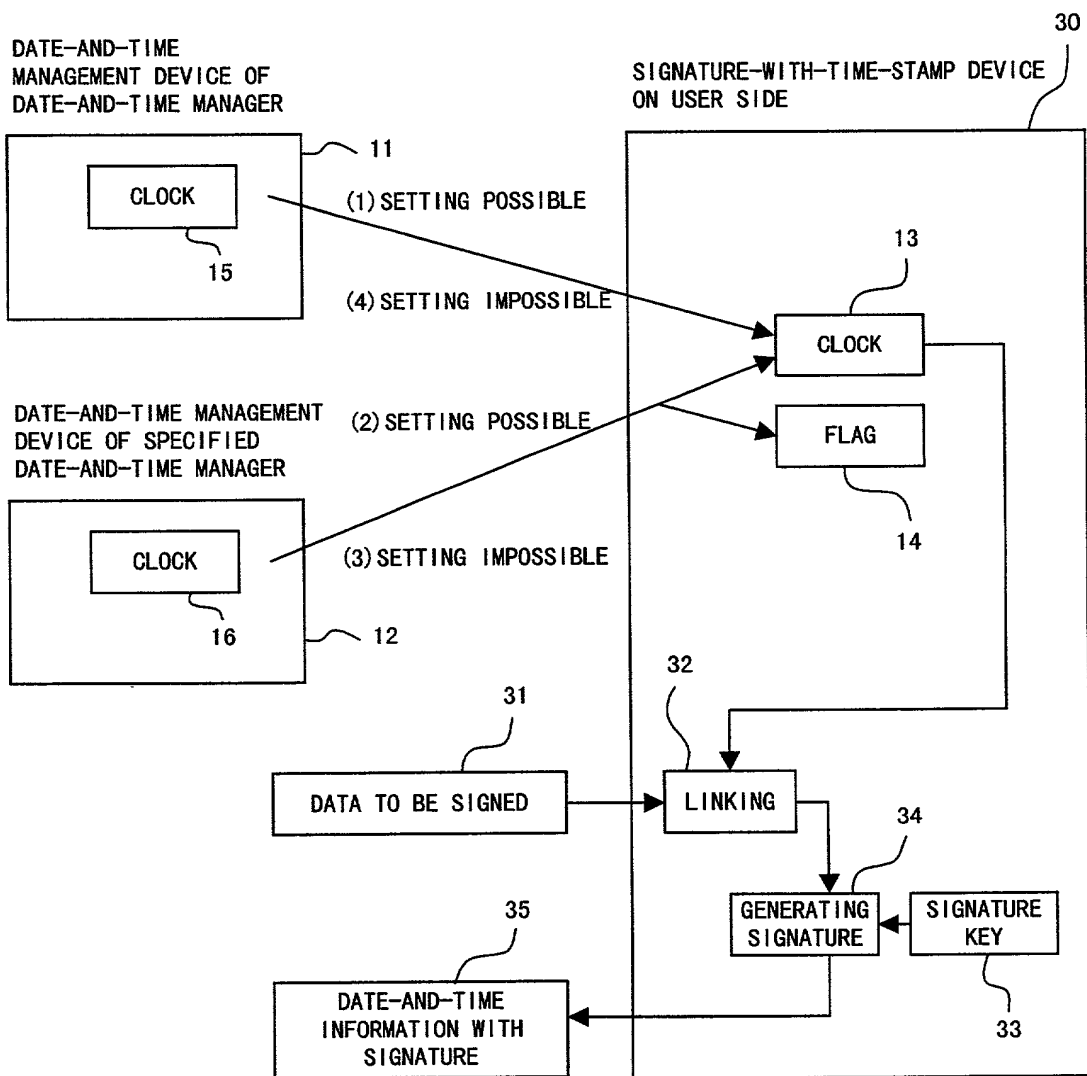


FIG. 4

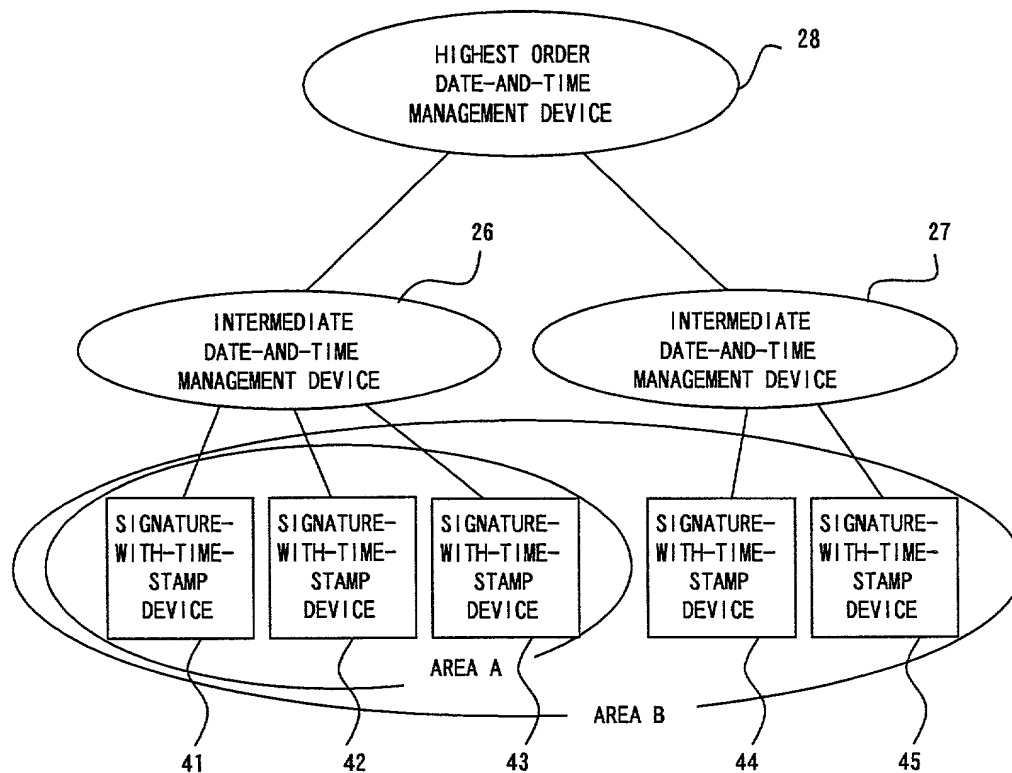


FIG. 5

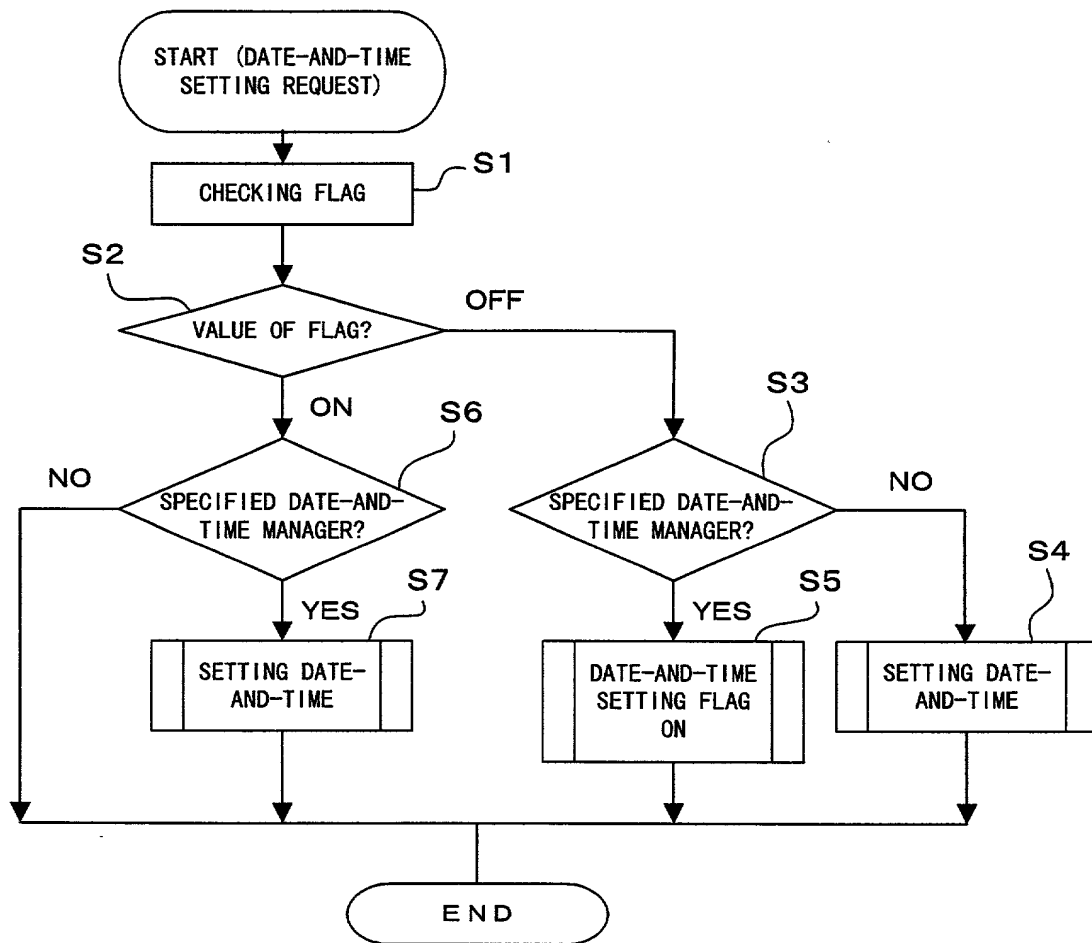


FIG. 6

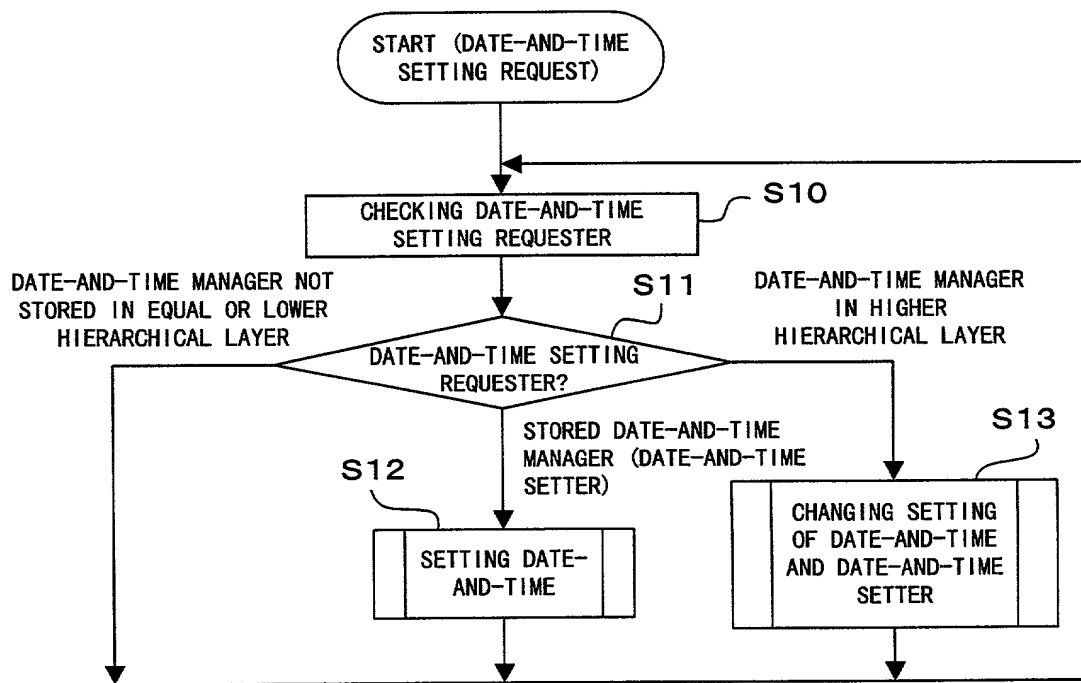


FIG. 7

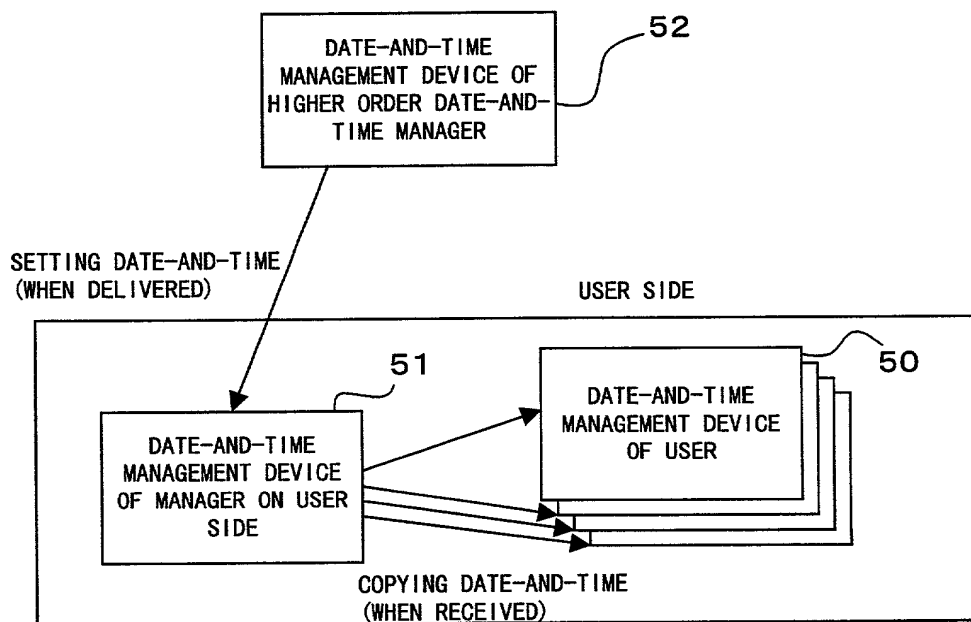


FIG. 8



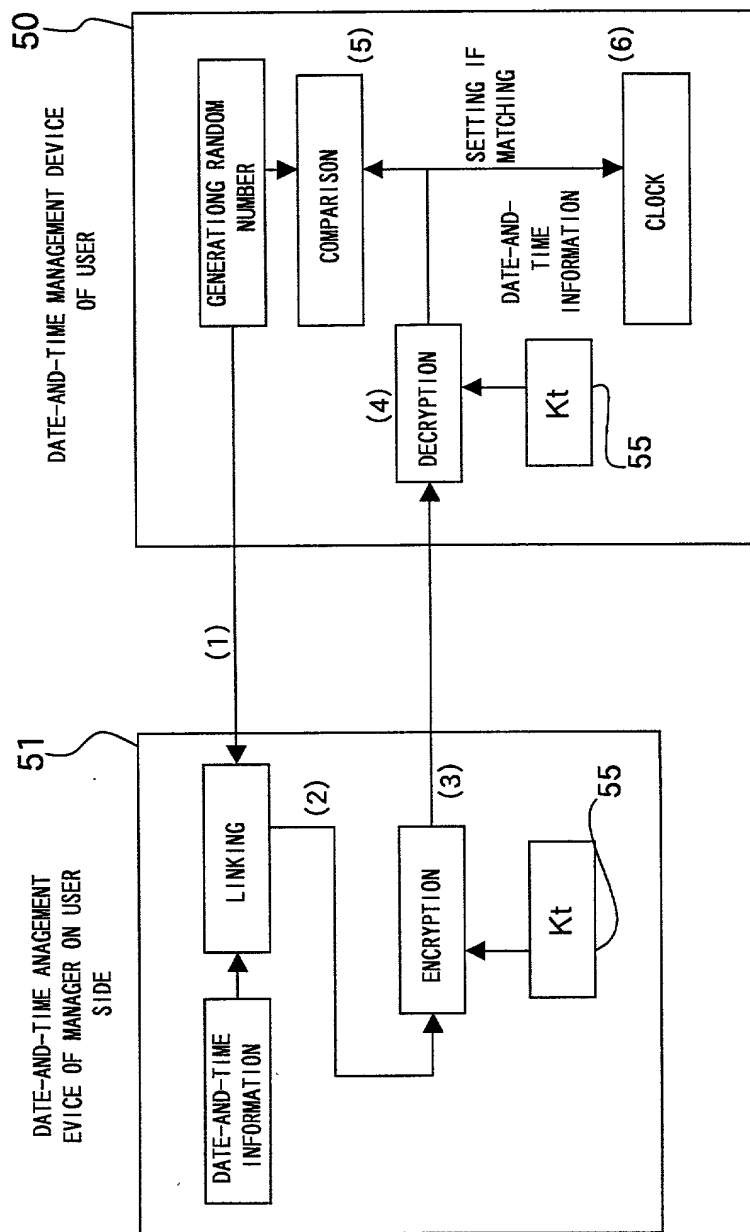


FIG. 9

FIG. 10 is a block diagram of a date-and-time management device of a user, showing the internal components and their interconnections.

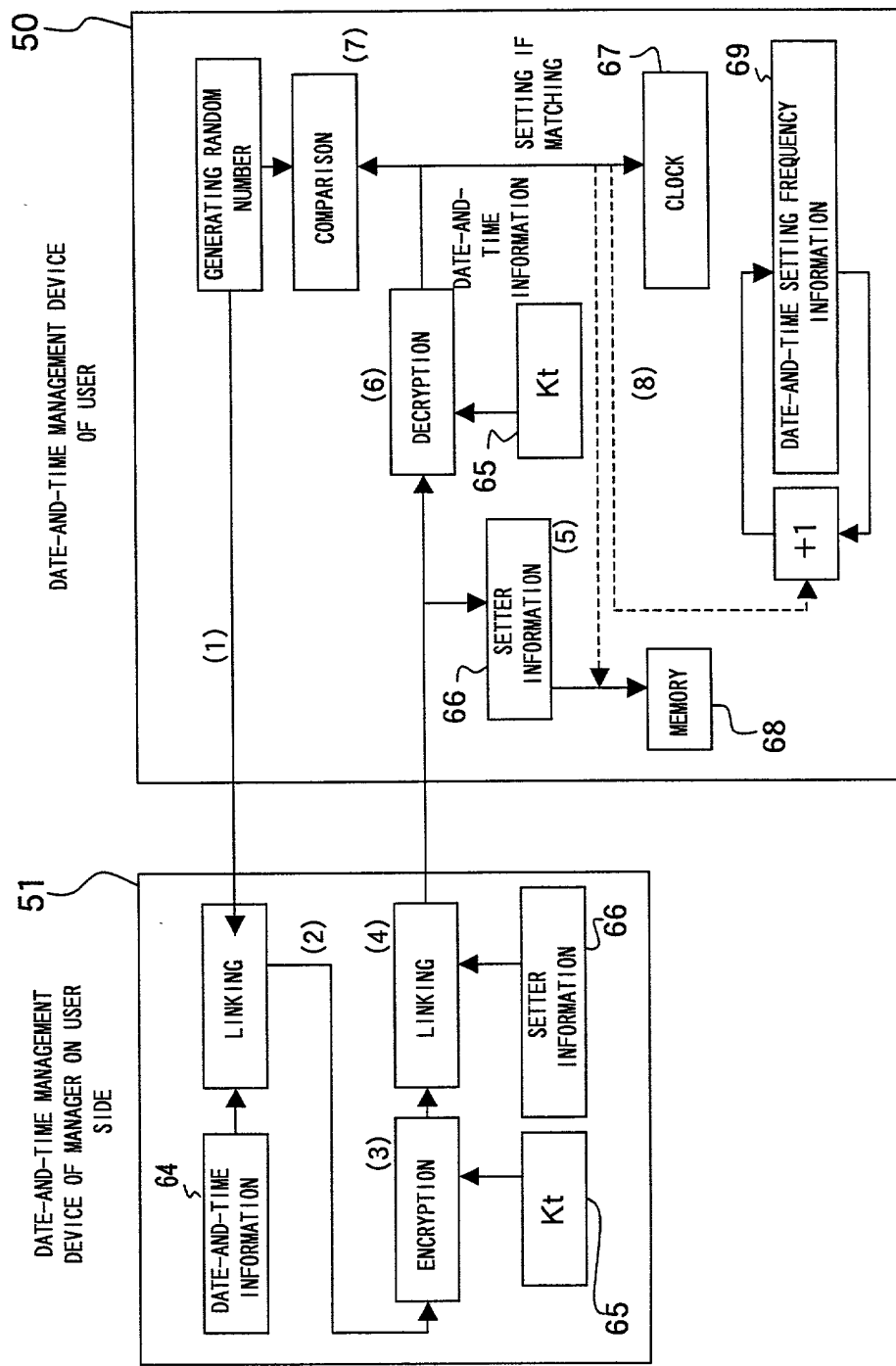


FIG. 10

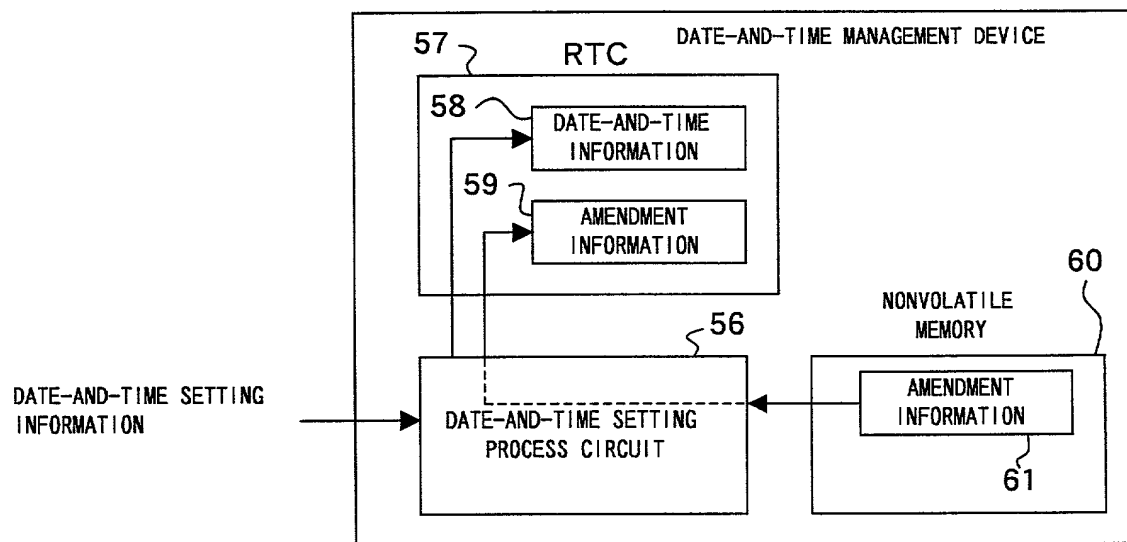


FIG. 11

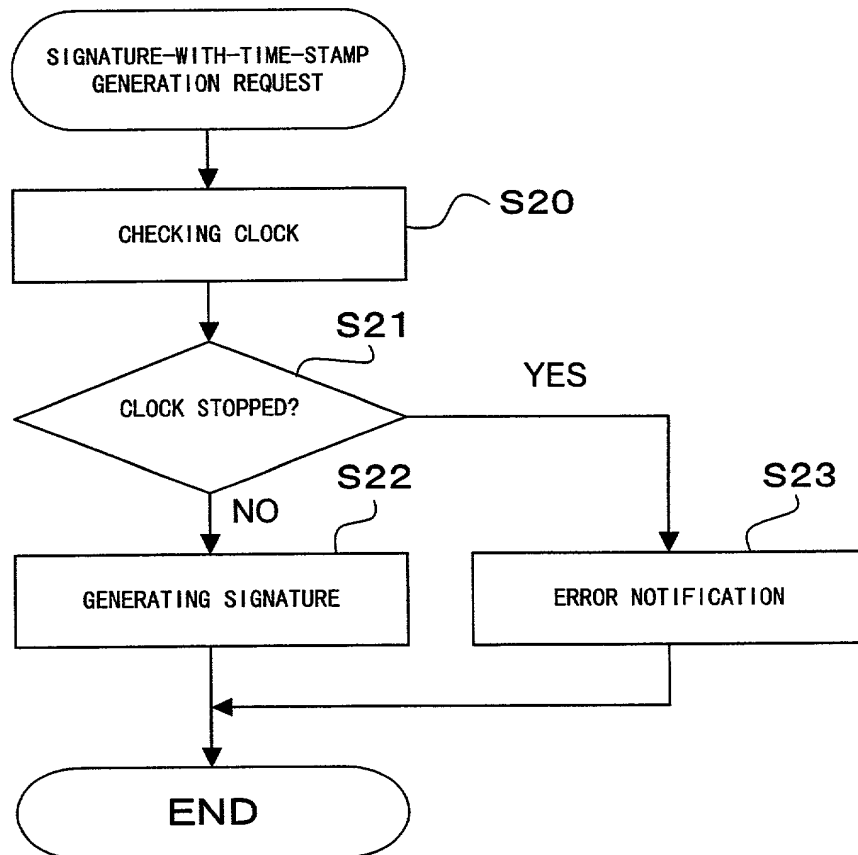


FIG. 12

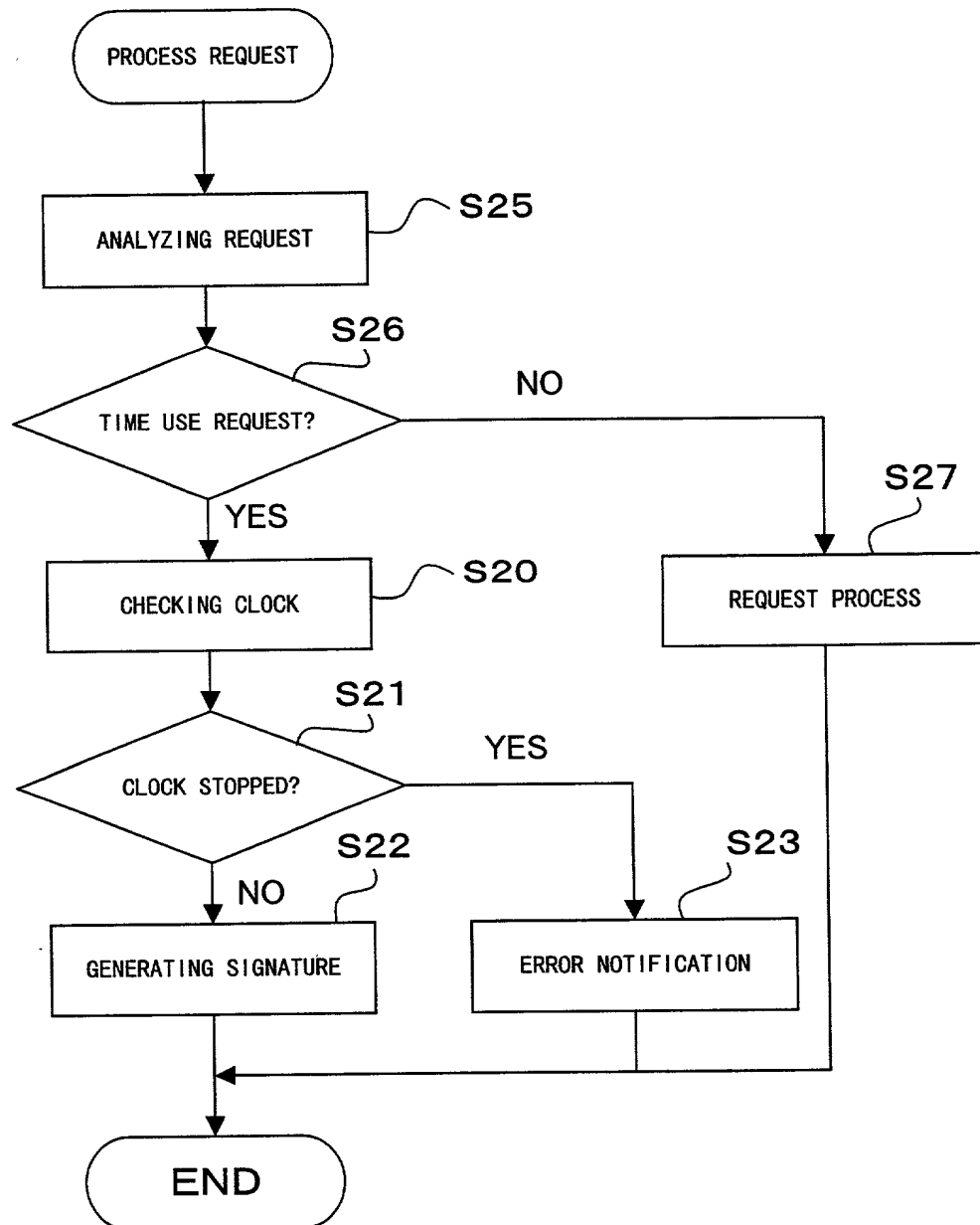


FIG. 13

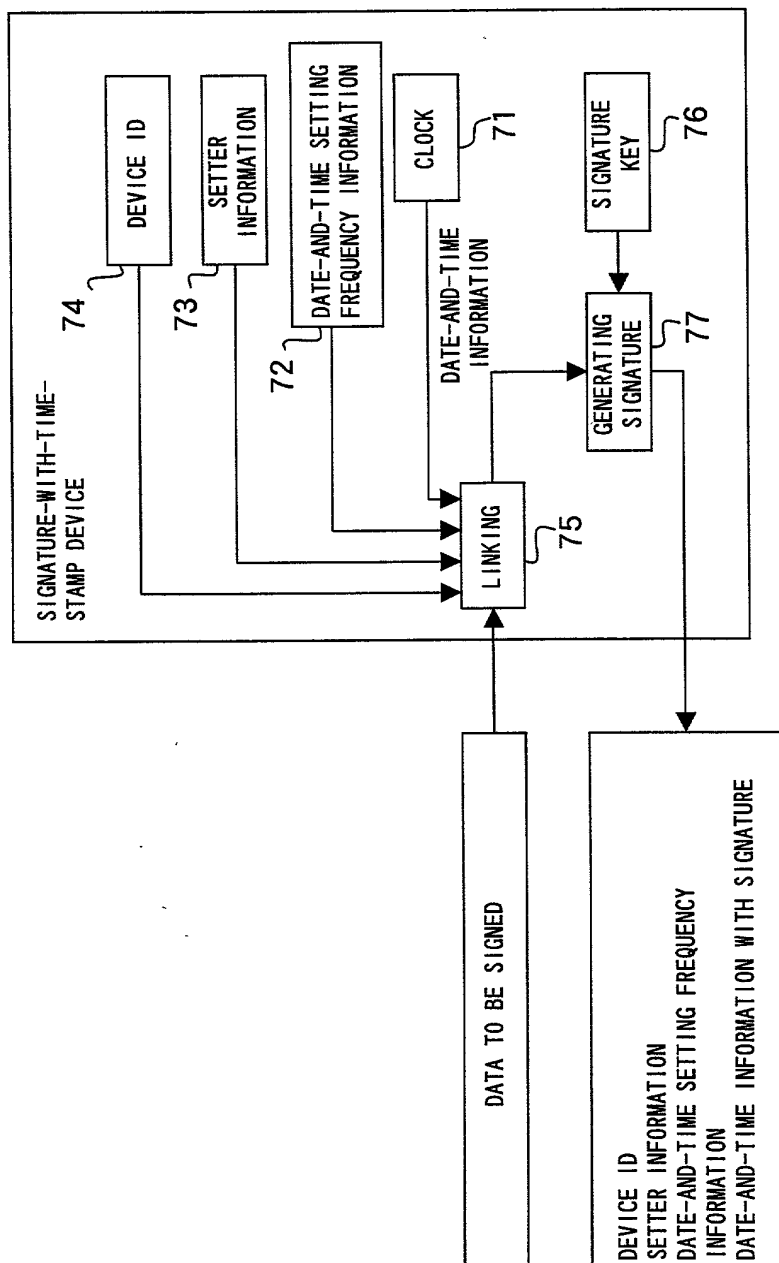


FIG. 14

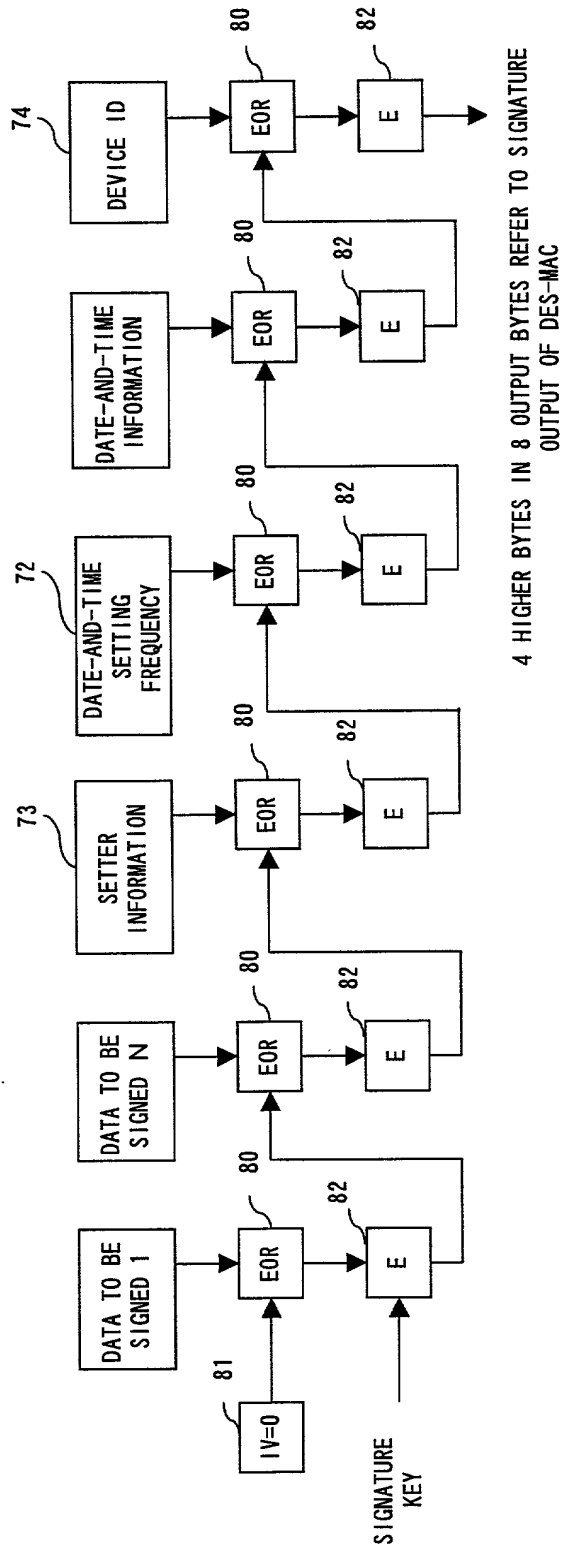


FIG. 15

FIG. 16 is a block diagram of a signature-with-time-stamp verification device 83. The device 83 includes a data input block 84, a regenerating signature block 85, a comparison block 86, a green LED 87, and a red LED 88. The data input block 84 receives data to be signed, device ID, setter information, date-and-time setting frequency information, and date-and-time information with signature. The regenerating signature block 85 generates a signature based on the received data. The comparison block 86 compares the generated signature with the received signature. If the comparison is successful (OK), the green LED 87 is activated. If the comparison fails (NG), the red LED 88 is activated.

83 SIGNATURE-WITH-TIME-STAMP VERIFICATION DEVICE

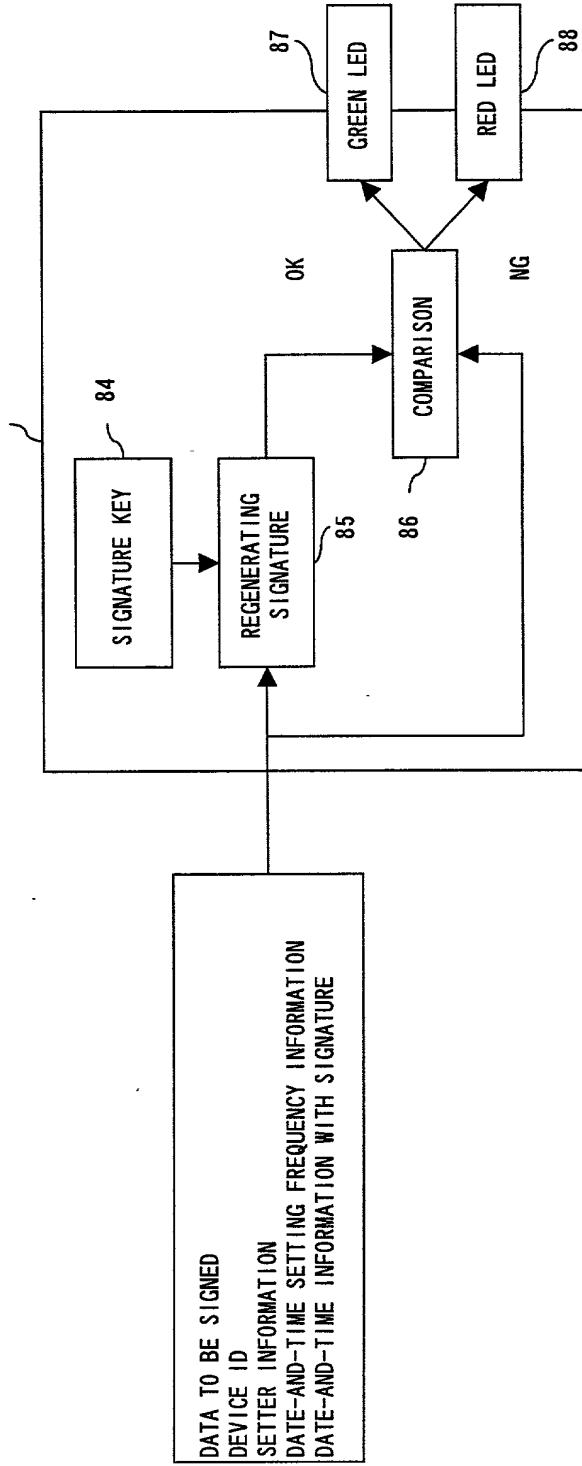


FIG. 16



FIG. 17 is a block diagram of a signature device 70. The device 70 includes a verifying signature block 91, a device key block 90, a user information block 92, a shortest password length block 93, and a retry frequency limit information block 94. The device 70 also includes a block for user information, shortest password length, retry frequency limit information, and signature. The device 70 is a signature device with time-stamp device.

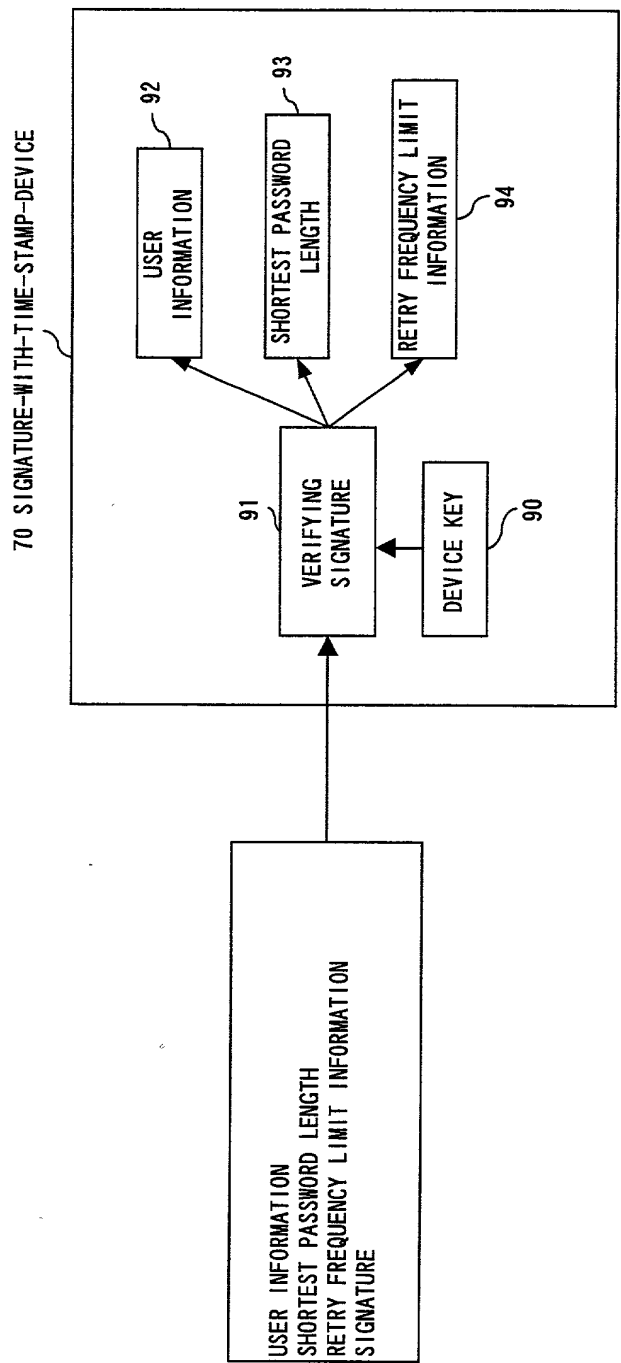


FIG. 17

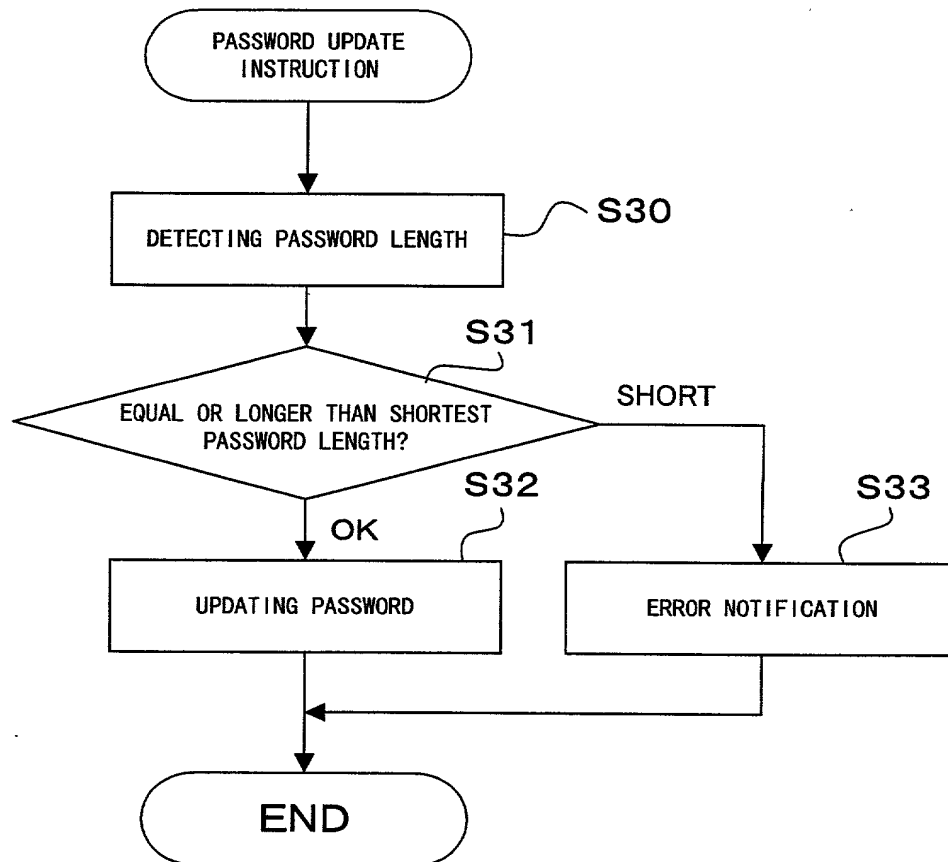


FIG. 18

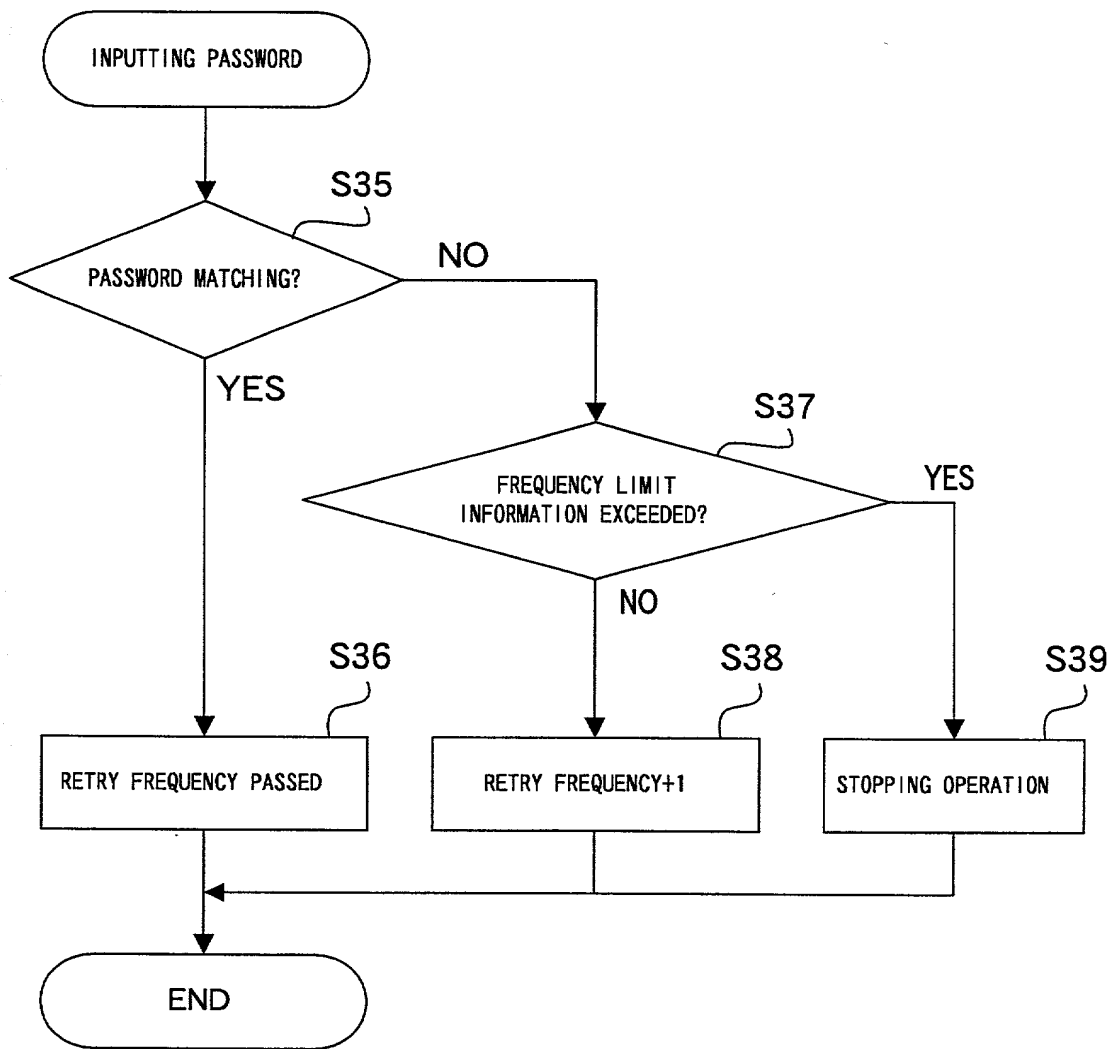


FIG. 19

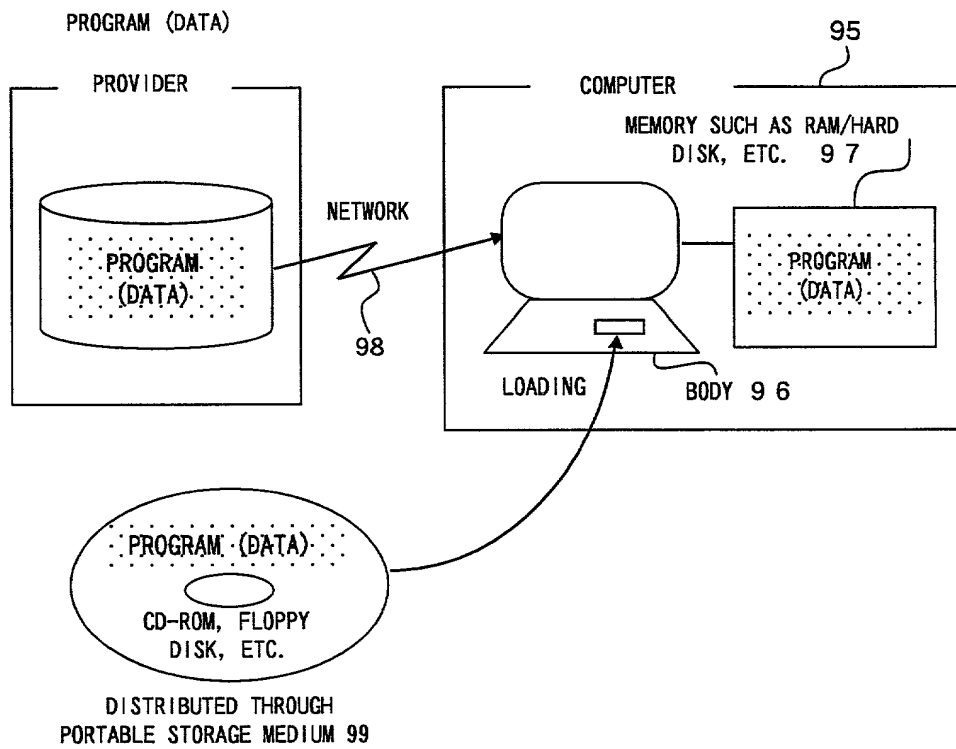


FIG. 20

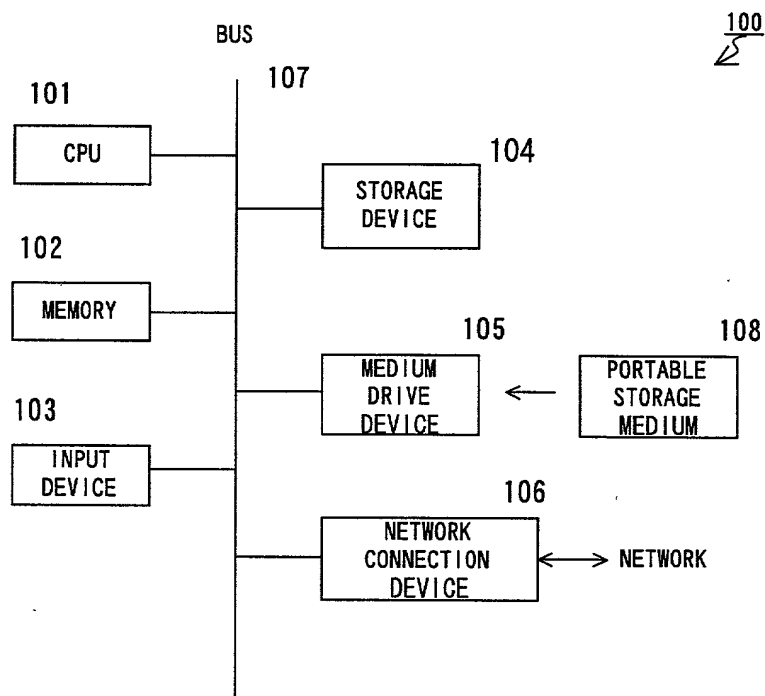


FIG. 21